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In re application of:

Garg et al.

Appl. No.: 09/188,708

Filed: November 10, 1998

For: RISC Microprocessor
Architecture Implementing
Multiple Typed Register Sets

Art Unit: 2783

Examiner: L. Donaghue

Atty. Docket: SP018.C4

**Terminal Disclaimer to Obviate a Double Patenting Rejection
under 37 C.F.R. § 1.321(c)**

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Seiko Epson Corporation represents that it is the owner of the entire right, title, and interest of Application No. 07/726,773, filed on July 8, 1991, now U.S. Patent No. 5,493,687, issued February 20, 1996 (the '687 patent) by virtue of two Assignments. The first Assignment from all of the inventors thereof to S-MOS Systems, Inc., was executed on September 6, 1991, and was recorded on September 30, 1991, at Reel 5885, Frames 196-199. Said Assignment also covers all continuations of the '687 patent. The second Assignment made by S-MOS Systems, Inc. to **Seiko Epson Corporation** was executed on September 13, 1994, and was recorded on September 19, 1994, at reel 7133, frames 724-728. As the above-named Application No. 09/188,708, co-pending Application No. 08/465,239, filed June 5, 1995, now U.S. Patent 5,560,035, issued September 24, 1996 (the '035 patent), co-pending Application No. 08/665,845, filed June 19, 1996, now U.S. Patent No. 5,682,546, issued October 28, 1997 (the '546 patent),

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Don Featherstone, Registration No. 33,876

Don Featherstone 11/1/99

Signature

Date

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and co-pending Application No. 08/937,361, filed September 25, 1997, now U.S. Patent No. 5,838,986, issued November 17, 1998 (the '986 patent) are continuations of the '687 patent, **Seiko Epson Corporation** is the owner of the entire right, title, and interest of the '687 patent, the '035 patent and the '546 patent and the above-captioned Application No. 08/937,361, by virtue of both of these Assignments.

Disclaimer

Seiko Epson Corporation, through its authorized undersigned representative, hereby disclaims the terminal part of any patent granted on the above-named application that would extend beyond the earliest expiration date of its co-owned U.S. Patent Nos. 5,493,687, 5,560,035, 5,682,546 and 5,838,986, and hereby agrees that any patent granted on the above-named application, or any patent granted on the above-named application and subject to the reexamination proceeding, shall be enforceable only for and during such period that said patent granted on the above-named application is commonly owned with the patents that could form the basis of an obviousness type double patenting rejection, this agreement to run with any patent granted on the above-named application and to be binding upon the grantee, its successors or assigns.

Seiko Epson Corporation does not disclaim any terminal part of any patent granted on the above-named application prior to the expiration date of the full statutory term, as currently shortened by any Terminal Disclaimer, of U.S. Patent Nos. 5,493,687, 5,560,035, 5,682,546 and/or 5,838,986 in the event that any of U.S. Patent Nos. 5,493,687, 5,560,035, 5,682,546 and/or 5,838,986 later expires for failure to pay a maintenance fee, are held unenforceable, are found invalid, are statutorily disclaimed in whole or terminally disclaimed under 37 C.F.R. § 1.321(a), have all claims canceled by a reexamination certificate, or are otherwise terminated prior to the expiration of their respective statutory terms as presently shortened by Terminal Disclaimer, except for the separation of legal title stated above.

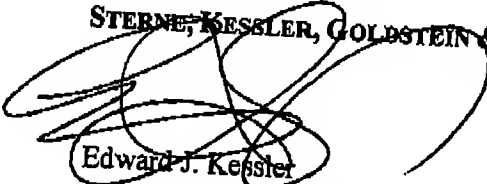
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The Commissioner is hereby authorized to charge the disclaimer fee of \$110.00 as set forth in 37 C.F.R. § 1.20(d) to our Deposit Account No. 19-0036.

Respectfully submitted,

~~STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.~~



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Date: 11/1/99
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